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The present invention relates to an in-line phase shifter and particularly to an in-line phase shifter that is both physically and electrically attached to the drive and input circuits of the mixer circuit of a radio receiver. The phase shifter of the present invention finds application in an incoming signal detector used in communication systems. Its application in the desired phase detector used in a narrow band selective filter is obvious.

The in-line phase shifter of the present

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invention is physically and electrically attachable to an incoming signal detector such as a transistor and mixer circuit. The gate of a transistor has a fixed capacitance. A signal detector would use this fixed capacitance to determine the presence of the incoming wave or signal. If the electrical path between the phase shifter and the gate of the transistor is short enough, the signal detector's output would not change when the phase shifter switched the transistor from on to off or when the transistor switched from off to on. A small phase difference between the incoming signal and the signal detector could cause the transistor to oscillate. The gate capacitance of a transistor is used as a reference capacitor to a phase comparator in a phase detector for the

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incoming signal. When the signals in the incoming and phase comparator have the same phase, the phase detector drives the transistor at a constant voltage level. When the signals in the incoming and phase comparator are 180 degrees out of phase, the phase detector drives the transistor at a second voltage level. The level of the second voltage is determined by the voltage divider formed by the phase shifter and the gate capacitance of the transistor. If the phase shifter is a high pass filter, the phase detector will drive the transistor to one voltage level when the transistor is on and to the other voltage level when the transistor is off. The second voltage level will be fixed by the voltage divider. A gate capacitance reference is desired to

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eliminate the unwanted oscillation of the transistor in the above application. The gate capacitance of the transistor is fixed. It is desired to increase the range 3e33713323

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